## SILICON-IP, INC.

3rd Party IP Acquisition & Management for SoC Design Teams

#### **SoC Designers**









#### **IP Providers**



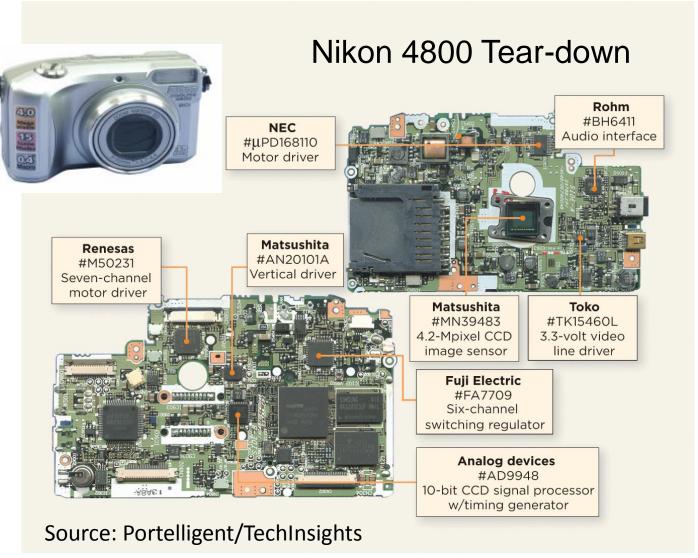




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## Silicon-IP Kit Methodology

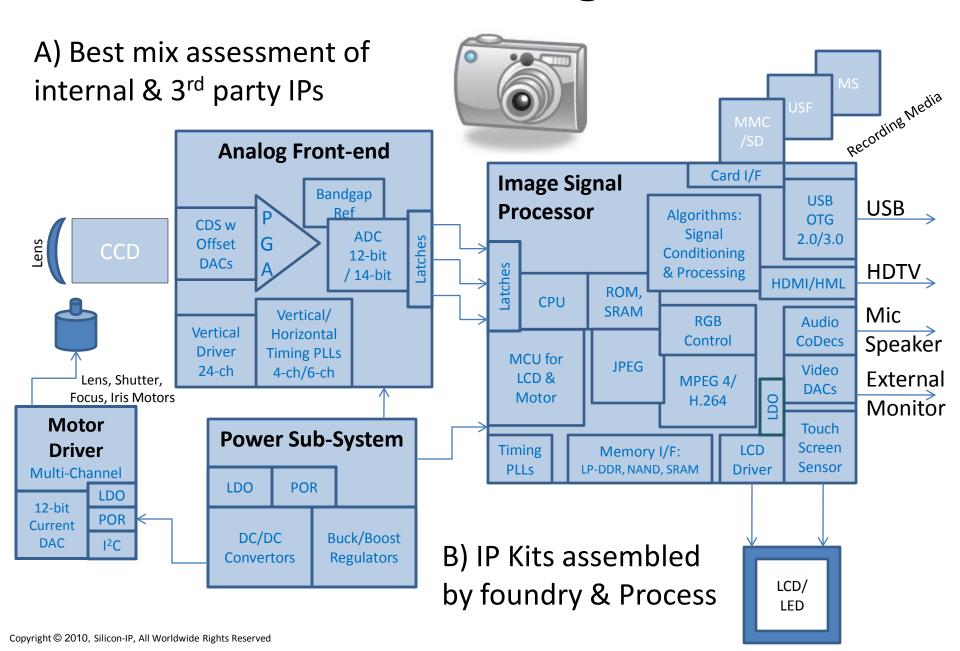




## DSC Example

IP evaluation & trade-offs aligned with target market specs

# DSC IP Kit – Block Diagram Drives



# IP Ranked by SoC Design Team Requirements

## 1) Customer Requirements

1<sup>st</sup> Independent Validation 2<sup>nd</sup> Critical Specs

3<sup>rd</sup> IP Kit Inter-operability

4<sup>th</sup> Support & Price

5<sup>th</sup> Si-verified

### 2) IP Relative Ranking

A. IP Opt 2

B. IP Opt 4

C. IP Opt 1

D. IP Opt 5

E. IP Opt 3



Silicon-IP Validated	IP Opt 1	IP Opt 2	IP Opt 3	IP Opt 4	IP Opt 5
Technical Parameters Validated				per	× ×
Active/Stand- by Power, Area, etc.			3/45	Mai	
Quality & Si-Validation		No.		D'S	•
Integration & Inter- operability Validation	J	idilic	sil se	360	
# Tape outs & Wafer Ships					

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