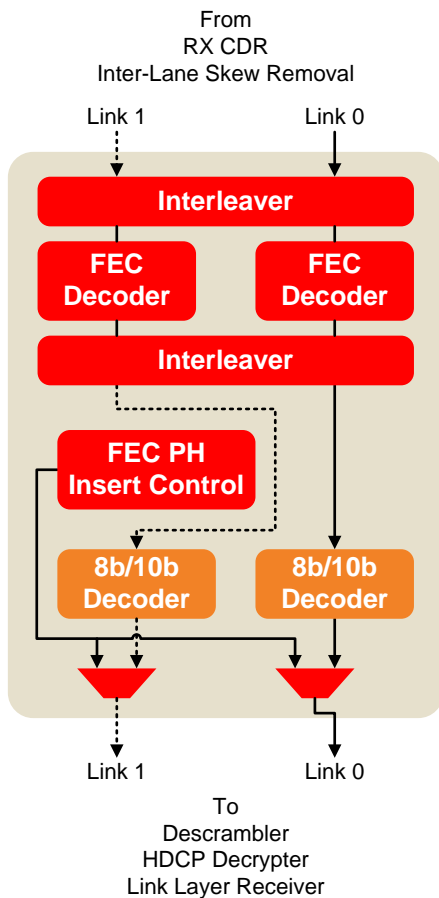


VESA DisplayPort 1.4 FEC RX IP Core

Applications

- UHD monitors
- DisplayPort 1.4 products
- USB Type-C products
- UHD TVs

Hardent DP1.4 FEC RX IP



Hardent provides IP solutions as well as cutting-edge ASIC and FPGA design services for electronics manufacturers using display technology. As a member of VESA and a key contributor of the DSC Task Group, Hardent has used its expertise and skills to develop the very latest standards in display technology. Hardent's high-quality IPs enable clients to accelerate their development schedules and meet demanding time-to-market deadlines.

Description

The DisplayPort™ Forward Error Correction (FEC) Receiver IP core implements Reed-Solomon FEC and symbol interleaving as specified by the VESA DisplayPort 1.4 specification. Forward Error Correction is required to ensure glitch-free Display Stream Compression (DSC) bitstream transport.

Key Features

- VESA DisplayPort 1.4 compliant
- Reed-Solomon (254,250) FEC, 10-bit symbols
- Two-way interleaving for 1-, 2-, and 4-lane modes (4-lane mode requires 2 FEC IP core instances)
- Optionally includes the DisplayPort main 8b/10b decoder

Deliverables

- Encrypted RTL source code IP core
- Functional and structural coverage reports
- Comprehensive integration guide
- Technical support and maintenance updates

Product Options

- IP customization and integration services available on request
- Multi-project licenses available
- UVM verification bindable modules



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