Course Description

The Portable Test and Stimulus Standard (PSS) from Accellera defines a way to capture the test intent of users for verification of IP, IP subsystems, and SoC designs from simulation to post-silicon.

This 2-day training course will introduce the student to the PSS language and to key concepts such asdeclarative programming, resource management, multi-target realization, and more.

The course is available in either "flavor" of PSS: DSL (Domain Specific Language) or C++.

The course is developed and taught by Willamette HDL using Cadence© Perspec™ System Verifier.

Level – PSS 1

Course Duration – 2 days

Price – $1600 per student
　　　　　Pricing for private classes available on request

Who Should Attend?

- Anyone interested in learning the new industry standard language for Portable Test and Stimulus (PSS)
- Cadence Perspec System Verifier users who want to incorporate either DSL or C++ into their flow.

Prerequisites

- Contact Hardent for more information

Software Tools

- Cadence Perspec System Verifier

After completing this comprehensive training, you will have the necessary skills to:

- Understand the constructs and syntax of the new PSS language, either DSL or C++
- Write PSS descriptions of the use-case for an IP block up to a whole S0C subsystem
- Use Cadence Perspec System Verifier to solve and expand the test scenarios you need for SoC verification
- Use Cadence Perspec System Verifier to connect your PSS descriptions to UVM and drive your IP and subsystem verification

Course Outline

- Introduction
  - o PSS – capturing verification intent
  - o PSS – portability
  - o Design/tool flow overview
- Hello World
  - o PSS concepts introduction
  - o PSS development/tool flow introduction
- PSS Modeling Concepts
  - o Dataflow
  - o Resources
  - o Components
  - o Inference
  - o Constraints
  - o Scenarios
  - o Scheduling
- Data Types
- Packages

- Putting Together a Test – Block Level
  - o Components
  - o Actions
  - o Activities
    - Action Traversal
    - Action Scheduling
    - Flow Control
  - o Flow Objects
  - o Resources
  - o Pools
  - o File Organization
- Test Realization
  - o Exec Blocks
  - o Procedural Interface
- Type Extension
- Randomization and Constraints
- Coverage
- Putting Together a Test – Subsystem Level
- Putting Together a Test – System Level

Register Today

Hardent offers courses to help designers produce fast, predictable, and efficient designs. For a detailed course list, visit www.hardent.com/ training or contact Hardent's Training Coordinator for additional information, to register for a class, or to schedule a private course.

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Course material created by: